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6-14-99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : Martin VORBACH et al.  
Serial No. : To Be Assigned 09/290342  
Filed : Herewith  
For : DATA PROCESSING SYSTEM  
Examiner : To Be Assigned  
Art Unit : To Be Assigned

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

SIR:

Kindly amend the above-identified application  
before examination, as set forth below.

IN THE CLAIMS:

Please cancel claims 1-14, without prejudice.

Please add the following new claims:

15. (New) A massively parallel data processing apparatus  
comprising:

a plurality of computing cells arranged in a  
multidimensional matrix, the plurality of computing  
cells capable of simultaneously manipulating a  
plurality of data, each of the plurality of  
computing cells including:

an input interface for receiving a  
plurality of input signals;

a plurality of logic members, at least one  
of the plurality of logic members coupled to

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